

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	2280	@ad<"20000221" and (register file) and (bi-directional) and controller and multiplexer	US-PGPUB; USPAT	OR	OFF	2006/04/15 19:16
L6	31	@ad<"20000221" and (register file) and ("or" adj3 controller ) and controller and multiplexer	US-PGPUB; USPAT	OR	OFF	2006/04/15 17:14
L9	2936	@ad<"20000221" and (register adj3 file) and (mux multiplexer)	US-PGPUB; USPAT	OR	OFF	2006/04/15 17:16
L10	1092	@ad<"20000221" and (register adj3 file) same (mux multiplexer)	US-PGPUB; USPAT	OR	OFF	2006/04/15 17:16
L11	181	@ad<"20000221" and (register adj3 file) same (mux multiplexer) same latch	US-PGPUB; USPAT	OR	OFF	2006/04/15 17:16
L12	94	@ad<"20000221" and (register adj3 file) same (mux multiplexer) same latch same (control controller)	US-PGPUB; USPAT	OR	OFF	2006/04/15 17:18
L14	9	@ad<"20000221" and ((register adj3 file) and (mux multiplexer) and (control controller)).ab	US-PGPUB; USPAT	OR	OFF	2006/04/15 17:18
L18	5	@ad<"20000221" and (register adj3 file) same (mux multiplexer) same latch same (control controller) same ("or" )	US-PGPUB; USPAT	OR	OFF	2006/04/15 17:25
L19	52	@ad<"20000221" and (pipelin\$ same (mux muxs) same (register adj3 file))	US-PGPUB; USPAT	OR	OFF	2006/04/15 17:30
L63	6	@ad<"20000221" and ((VLSI adj3 circuit) same (register adj3 file))	US-PGPUB; USPAT	OR	OFF	2006/04/15 19:17